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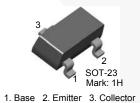
September 2015

# MPSA05 / MMBTA05 NPN General-Purpose Amplifier

# **Description**

This device is designed for general-purpose amplifier applications at collector currents to 300 mA. Sourced from process 10.





# **Ordering Information**

Part Number	Marking	Package	Packing Method
MPSA05RA	MPSA05	TO-92 3L	Tape and Reel
MMBTA05	1H	SOT-23 3L	Tape and Reel

# **Absolute Maximum Ratings**

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at  $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Value	Unit
V <sub>CEO</sub>	Collector-Emitter Voltage	60	V
V <sub>CBO</sub>	Collector-Base Voltage	60	V
V <sub>EBO</sub>	Emitter-Base Voltage	4.0	V
I <sub>C</sub>	Collector Current - Continuous	500	mA
$T_{J}$ , $T_{STG}$	Junction and Storage Temperature	-55 to +150	°C

# **Thermal Characteristics**

Values are at T<sub>A</sub> = 25°C unless otherwise noted.

Symbol	Parameter	Ma	Unit	
	Falamete	MPSA05	MMBTA05 <sup>(1)</sup>	Oilit
В	Total Device Dissipation	625	350	mW
P <sub>D</sub>	Derate Above 25°C	5.0	2.8	mW/°C
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case	83.3		°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	200	357	°C/W

### Note:

1. Device mounted on FR-4 PCB 1.6" × 0.06"

# **Electrical Characteristics**

Values are at  $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Max.	Unit
V <sub>(BR)CEO</sub>	Collector-Emitter Breakdown Voltage <sup>(2)</sup>	I <sub>C</sub> = 1 mA, I <sub>B</sub> = 0	60		V
V <sub>(BR)EBO</sub>	Emitter-Base Breakdown Voltage	$I_E = 100  \mu A, I_C = 0$	4		V
I <sub>CEO</sub>	Collector Cut-Off Current	$V_{CE} = 60 \text{ V}, I_{B} = 0$		0.1	μΑ
I <sub>CBO</sub>	Collector Cut-Off Current	$V_{CB} = 60 \text{ V}, I_{E} = 0$		0.1	μΑ
h <sub>FE</sub>	DC Current Gain	$I_C = 10 \text{ mA}, V_{CE} = 1.0 \text{ V}$	100		
		$I_C$ = 100 mA, $V_{CE}$ = 1.0 V	100		
V <sub>CE</sub> (sat)	Collector-Emitter Saturation Voltage	I <sub>C</sub> = 100 mA, I <sub>B</sub> = 10 mA		0.25	V
V <sub>BE</sub> (on)	Base-Emitter On Voltage	I <sub>C</sub> = 100 mA, V <sub>CE</sub> = 1.0 V		1.2	V
f <sub>T</sub>	Current Gain - Bandwidth Product	I <sub>C</sub> = 10 mA, V <sub>CE</sub> = 2 V, f = 100 MHz	100		MHz

### Note:

2. Pulse test: pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2.0%.

# **Physical Dimensions** 3.44 2.54 13.00 10.50 0.56 2.80 2.40 NOTES: UNLESS OTHERWISE SPECIFIED DRAWING CONFORMS TO JEDEC MS-013, VARIATION AC. ALL DIMENSIONS ARE IN MILLIMETERS. DRAWING CONFORMS TO ASME Y14.5M-2009. DRAWING FILENAME: MKT-ZA03FREV3. FAIRCHILD SEMICONDUCTOR. 4.19 3.05 2.66 2.13

Figure 1. 3-Lead, TO-92, Molded, 0.2 In Line Spacing Lead Form

# Physical Dimensions (Continued) 0.95 2.92±0.20 3 1.40 1.30+0.20 2.20 2 0.60 (0.29) -0.37 0.95 ⊕ 0.20M A B 1.00 1.90 1.90 LAND PATTERN RECOMMENDATION 1.20 MAX SEE DETAIL A (0.93)0.10 0.00 △ 0.10(M) C С 2.40±0.30 NOTES: UNLESS OTHERWISE SPECIFIED **GAGE PLANE** A) REFERENCE JEDEC REGISTRATION TO-236, VARIATION AB, ISSUE H. B) ALL DIMENSIONS ARE IN MILLIMETERS. 0.23 C) DIMENSIONS ARE INCLUSIVE OF BURRS, 0.08 0.25 MOLD FLASH AND TIE BAR EXTRUSIONS. D) DIMENSIONING AND TOLERANCING PER ASME Y14.5M - 1994. 0.20 MIN E) DRAWING FILE NAME: MA03DREV10 SEATING **PLANE** (0.55)**DETAIL A** SCALE: 2X

Figure 2. 3-LEAD, SOT23, JEDEC TO-236, LOW PROFILE





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Definition of Terms			
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Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.	
No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.	
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